

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): NOGUCHI et al

Serial No.: 09/825,946

Filed: April 5, 2001

Semiconductor Integrated Circuit Device And Fabrication Method For:

For Semiconductor Integrated Circuit Device

2811 Group:

Examiner:

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

C 2800 MAIL Fig. 2000 October 29, 2000

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE SPECIFICATION:

Please replace the original specification with the attached Substitute Specification.